

**In the Claims**

1-5. (Canceled)

5        6. (Currently Amended) The apparatus of Claim 5, An apparatus for checking a  
status of a light emitting device, the apparatus comprising:  
          a light emitting device;  
          a voltage supply coupled to the light emitting device for making a current flow  
through the light emitting device;  
10        an electrically conductive device spaced apart from the light emitting device for  
generating a voltage in response to the current flowing through the light emitting device;  
          a status determining device for determining the status of the light emitting device  
based on the voltage from the electrically conductive device; and  
          a rectifier circuit having a first node and a second node, wherein the first node is  
15        connected to the electrically conductive device and the second node is connected to a signal  
detector,  
          wherein the voltage supply is an AC voltage supply, further comprising an RC filter  
          circuit for converting an output from the rectifier circuit into a DC signal, wherein the RC  
          filter circuit is connected to the rectifier circuit and the signal detector.

20

7. (Canceled)

25        8. (Currently Amended) The apparatus of Claim 7 further comprising An  
apparatus for checking a status of a light emitting device, the apparatus comprising:  
          a light emitting device;  
          a voltage supply coupled to the light emitting device for making a current flow  
through the light emitting device;  
          an electrically conductive device spaced apart from the light emitting device for  
          generating a voltage in response to the current flowing through the light emitting device; and  
30        a status determining device for determining the status of the light emitting device  
          based on the voltage from the electrically conductive device,  
          wherein the voltage having a level below a threshold value indicates that the light  
          emitting device is operating abnormally, and

5                   a dimming error prevention circuit coupled to a node between the electrically conductive device and the status determining device, wherein the dimming error prevention circuit is for connecting the status determining device to an alternative voltage supply if the light emitting device is in a normal inactive state, so as to prevent the status determining device from interpreting the voltage generated by the light emitting device in a normal inactive state as the light emitting device being in an abnormal inactive state.

9. (Original) The apparatus of Claim 8, wherein the dimming error prevention circuit comprises:

10                  a transistor;  
                        an alternative voltage source coupled to the transistor; and  
                        a dimming signal source coupled to the base of the transistor and generating an “on” signal upon detecting that the light emitting device is in a normal inactive state;  
                        wherein the transistor is configured to connect the alternative voltage source to the  
15                  status determining device in response to the “on” signal from the dimming signal source.

20                  10. (Original) The apparatus of Claim 8, wherein the dimming error prevention circuit comprises a transistor for supplying an alternative voltage to the status determining device upon receiving a signal indicating that the light emitting device is in a normal inactive state.

25                  11. (Original) The apparatus of Claim 8 further comprising a time constant determining circuit coupled to the node for creating a delay in the dimming error prevention circuit’s disconnecting of the alternative voltage supply from the status determining device upon the light emitting device’s transition between an inactive state and a normal active state.

12-13. (Canceled)

30                  14. (Original) An apparatus for checking a status of a light emitting device, the apparatus comprising:  
                        a plurality of light emitting devices arranged in a predefined configuration;  
                        a voltage supply coupled to the light emitting devices for making a current flow through the light emitting devices;

10 a plurality of electrically conductive devices positioned a predetermined distance away from the light emitting devices, wherein each of the electrically conductive devices generates a voltage in response to current flowing through one of the light emitting devices; 5 an AND gate coupled to the electrically conductive devices such that the voltage from each of the electrically conductive devices forms an input to the AND gate, the AND gate generating an output; and a status determining device coupled to the output of the AND gate to determine the status of the light emitting device based on the output.

15 10 15. (Original) The apparatus of Claim 14, wherein the status determining device comprises a means for stopping the current from flowing through the light emitting devices if the output of the AND gate fulfills a predefined condition.

15 16. (Original) The apparatus of Claim 14 further comprising a plurality of rectifier circuits, wherein each of the rectifier circuits is coupled to one of the electrically conductive devices and to the AND gate.

20 17. (Original) The apparatus of Claim 16, wherein the voltage supply is an AC voltage supply, further comprising a plurality of conversion circuits for receiving an AC signal from the rectifier circuits and generating a DC voltage for inputting into the AND gate.

25 18. (Original) The apparatus of Claim 17, wherein the conversion circuits comprise an RC filter.

30 19. (Original) The apparatus of Claim 18 further comprising a dimming error prevention circuit coupled to a node between the AND gate and the status determining device, wherein the dimming error prevention circuit is for connecting the status determining device to an alternative voltage supply if at least one of the light emitting devices is in a normal inactive state, so that the status determining device receives a different output voltage from the AND gate when one of the light emitting devices is in a normal inactive state than when one of the light emitting devices is in an abnormal inactive state.

20. (Original) The apparatus of Claim 19, wherein the dimming error prevention circuit comprises:

a transistor;  
an alternative voltage source coupled to the transistor; and  
a dimming signal source coupled to the base of the transistor and generating an “on”  
signal upon detecting that the light emitting device is in a normal inactive state;  
5 wherein the transistor is configured to connect the alternative voltage source to the  
status determining device in response to the “on” signal from the dimming signal source.

10 21. (Original) The apparatus of Claim 19, wherein the dimming error prevention  
circuit comprises a transistor for supplying an alternative voltage to the status determining  
device upon receiving a signal indicating that the light emitting device is in a normal inactive  
state.

15 22. (Original) The apparatus of Claim 19 further comprising a time constant  
determining circuit coupled to the node for creating a delay in the dimming error prevention  
circuit’s disconnecting of the alternative voltage supply from the status determining device  
upon the light emitting device’s transition between an inactive state and a normal active state.

23-25. (Canceled)

20 26. (Currently Amended) ~~The method of Claim 24 further comprising A method of  
checking a status of a light emitting array, wherein the light emitting array includes a plurality  
of light emitting devices, the method comprising:~~

25 ~~generating a voltage in response to a current flowing through each of the light  
emitting devices by using an electrically conductive device spaced apart from the light  
emitting device;~~  
~~processing the voltage of the light emitting devices to generate an output voltage;  
determining whether any of the light emitting devices is in an abnormal operational  
state based on the output voltage; and~~  
30 ~~stopping the current flowing through each of the light emitting devices upon  
determining that at least one of the light emitting devices is in an abnormal operational state.~~

27. (Canceled)

28. (Canceled)

29. (Currently Amended) ~~The liquid crystal display device of Claim 27 further comprising:~~ A liquid crystal display device comprising:  
5                   a liquid crystal display panel;  
                 electrodes for generating an electric field in the liquid crystal display panel; and  
                 a backlight assembly including:  
                 a first light emitting device;  
                 a voltage supply coupled to the light emitting device for making a first current  
                 flow through the first light emitting device;  
10                  a first electrically conductive device spaced apart from the light emitting device for  
                 generating a first voltage in response to the first current flowing through the first light  
                 emitting device;  
                 a status determining device for determining the status of the light emitting device  
                 based on the first voltage from the electrically conductive device;  
15                  a second light emitting device coupled to the voltage supply for making a second  
                 current flow through the second light emitting device;  
                 a second electrically conductive device positioned for generating a second voltage  
                 in response to the second current; and  
                 an AND gate receiving the first voltage and the second voltage and generating an  
20                  output voltage, wherein the output voltage is coupled to the status determining device, which  
                 determines the status based on the output voltage of the AND gate.

30. (Original) The liquid crystal display device of Claim 29 further comprising a  
dimming error prevention circuit coupled to a node between the AND gate and the status  
25                  determining device, wherein the dimming error prevention circuit comprises:  
                 a transistor;  
                 an alternative voltage source coupled to the transistor; and  
                 a dimming signal source coupled to the base of the transistor and generating an “on”  
                 signal upon detecting that the light emitting device is in a normal inactive state;  
30                  wherein the transistor is configured to connect the alternative voltage source to the  
                 status determining device in response to the “on” signal from the dimming signal source.

31. (Original) The liquid crystal display device of Claim 29 further comprising a  
dimming error prevention circuit coupled to a node between the AND gate and the status

determining device, wherein the dimming error prevention circuit comprises a transistor for supplying an alternative voltage to the status determining device upon receiving a signal indicating that the light emitting device is in a normal inactive state.

5           32. (Original) The liquid crystal display device of Claim 31 further comprising a time constant determining circuit coupled to the node for creating a delay in the dimming error prevention circuit's disconnection of the alternative voltage from the status determining device when the light emitting device transitions between an inactive state and a normal active state.